



April 19-21 Dresden, Germany

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[www.ddecs.org/ddecs2017](http://www.ddecs.org/ddecs2017)

## Call for Papers

The IEEE International Symposium on Design and Diagnostics of Electronic Circuits and Systems provides a forum for exchanging ideas, discussing research results, and presenting practical applications in the areas of design, test, and diagnosis of electronic circuits and systems. The DDECS Symposium series has been organized by these European countries: Czech Republic (1997, 2002, 2006, 2009, 2013), Poland (1998, 2003, 2007, 2014), Slovakia (2000, 2004, 2008, 2016), Hungary (2001, 2005), Austria (2010), Germany (2011), Estonia (2012), and Serbia (2015).

#### Topics of interest include but are not limited to:

<i>SoC and NoC Design and Test</i>	<i>Design and Test in Nano-Technologies</i>
<i>ASIC/FPGA Design</i>	<i>Analog, Mixed-Signal, RF Design and Test</i>
<i>Built-in Self-Test and Self-Repair</i>	<i>ATE Hardware and Software</i>
<i>Bio-Inspired Hardware</i>	<i>Design for Testability and Diagnosis</i>
<i>Design Verification/Validation</i>	<i>On-line Testing</i>
<i>Formal Methods in System Design</i>	<i>Embedded Systems</i>
<i>Hardware/Software Co-Design</i>	<i>Memory, Processor Testing</i>
<i>IP-based Design</i>	<i>MEMS Testing</i>
<i>Logic Synthesis</i>	<i>Physical Design</i>
<i>Defect/Fault Tolerance and Reliability</i>	<i>Cyber Physical Systems</i>

#### Submissions

Prospective authors are cordially invited to submit original papers using the electronic submission system at the Symposium web page. Papers in English with a length of 6 pages maximum in IEEE conference style are expected. Specialized student and industrial sessions, as well as embedded tutorials, will be organized at the symposium. Accepted papers will be included in the Symposium Proceedings and submitted for inclusion into IEEE Xplore as well as other Abstracting and Indexing databases (WoS, Scopus, etc). The best papers from the symposium will be invited for submission to IEEE Design & Test of Computers and JETTA journals. An extra work in progress session will be targeted to get early feedback on in-progress research and preliminary results.

#### Important Dates

Submission deadline:	<b>10<sup>th</sup> January 2017</b>
Notification of acceptance:	<b>04<sup>th</sup> March 2017</b>
Camera-ready papers:	<b>15<sup>st</sup> March 2017</b>

#### Symposium Location

Dresden, the capital of Saxony, is situated on the river Elbe near the borders with Poland and Czech Republic. Dresden is the biggest microelectronic center in Europe. Dresden has some universities, the biggest is the Technical University. Institutes and divisions from the German major research organisation, like Fraunhofer, Helmholtz and Leibniz, are settled in Dresden. Dresden has a lot theatres, museums and galleries. Dresden has a long history in culture, industry, and academic research.

#### Further Information

General Chair <b>Manfred Dietrich</b> Fraunhofer Institute for Integrated Circuits Division Engineering of Adaptive Systems	Program Chair <b>Ondrej Novak</b> Technical University Liberec Institute for Information Technology and Electronics
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